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APPLICANTS

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**** CONTINUING DATA *******

This application is a 371 of PCT/JP2005/005288 03/23/2005

**** FOREIGN APPLICATIONS *******

JAPAN 2004-084352 03/23/2004
 JAPAN 2004-292214 10/05/2004

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

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Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		JAPAN	20	21	6

ADDRESS

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TITLE

Clock Signal Generation Device, Semiconductor Integrated Circuit, and Data Reproduction Method

FILING FEE RECEIVED 1550	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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